

# A Tabular Expressions Toolbox for Critical Systems Development in Simulink

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  - Previous Table Tools & Workflows
- 3 The Tabular Expression Toolbox
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- 4 Handling IEEE Floating Point Numbers
- 5 Conclusions and Future Research

# Formal Specifications are Good!

- Give a precise description of required behavior of a system.
- Usually involve quite a bit of mathematical notation.

Claims about formal methods:

- Can be analyzed using sophisticated tools
  - help to find design faults earlier,
  - find faults that are unlikely to be found by other methods.
- Can be used to support testing.
- Help developers to produce better systems.
- Help maintainers to evolve the system effectively.
- Can help meet DO-178c via DO-333.

# So ... Why Don't People Use Them?

The  $W0_k(x)$  process describes a watchdog where the last input toggle was to 0 and there are  $x$  steps left until shut-down trigger.  $W1_k$  is the same except that the last input toggle was to 1.

$$W0_k(0) = [!\{d\} ?X \rightarrow W0_k(0)]$$

$$W1_k(0) = [!\{d\} ?X \rightarrow W1_k(0)]$$

$\forall x \geq 1 :$

$$W0_k(x) = [!\emptyset ?X \rightarrow \\ W1_k(k) \triangleleft (w \in X) \triangleright W0_k(x-1)]$$

$$W1_k(x) = [!\emptyset ?X \rightarrow \\ W0_k(k) \triangleleft (w \notin X) \triangleright W1_k(x-1)] \quad (29)$$

- Writing *and* reading the specifications is hard.
- There are often errors in the specifications.
- Specifications aren't (kept) consistent with the code.
- Tools don't add enough value to justify the effort.

# Tabular Expressions for Software Development

- Have been used extensively at NRL (e.g. A-7) [Heninger, 1980].
- Tabular expressions were used at Darlington Nuclear Generator for Shutdown Systems requirements and design documents.
- Readable by domain engineers, operators, testers . . . and developers!
- Used properly, a Formal development process using tabular expressions can help meet DO-178c (via DO-333).

# Tabular Expressions - A Usable “Formal” Method

## Example

### 2-Dimensional Table

$$f(x, y) = \begin{cases} x^2 - y^2, & ((y < 0) \wedge (x < 0)) \vee ((y < 0) \wedge (x > 0)) \vee ((x = 0) \wedge (y = 0)) \\ x + y, & ((y = 0) \wedge (x < 0)) \vee ((y = 0) \wedge (x > 0)) \vee ((y > 0) \wedge (x = 0)) \\ x^2 + y^2, & ((y < 0) \wedge (x = 0)) \vee ((x < 0) \wedge (y > 0)) \vee ((x > 0) \wedge (y > 0)) \end{cases}$$

$x < 0$	$x = 0$	$x > 0$
---------	---------	---------

$f(x,y) =$	$y < 0$	$x^2 - y^2$	$x^2 + y^2$	$x^2 - y^2$
	$y = 0$	$x + y$	$x^2 - y^2$	$x + y$
	$y > 0$	$x^2 + y^2$	$x + y$	$x^2 + y^2$

## So why isn't everyone using tables?

They only show significant benefits when used in a process with integrated tool support - which was (generally) lacking - until now! ;-)

# Tabular Expressions

- Pioneered by David Parnas.
- Represent mathematical conditional expressions formally and graphically.

## Example

Let  $x$  be a real valued variable. Then the *sign* function and its equivalent tabular representation are:

$$\text{sign}(x) = \begin{cases} -1, & x < 0 \\ 0, & x = 0 \\ 1, & x > 0 \end{cases} \iff \begin{array}{|c|c|c|} \hline x < 0 & x = 0 & x > 0 \\ \hline -1 & 0 & 1 \\ \hline \end{array}$$



# Tabular Expressions

- In order for a table to be proper it must satisfy two properties.

$$f(x_1, \dots, x_m) = \begin{array}{|c|c|c|c|} \hline c_1 & c_2 & \dots & c_n \\ \hline e_1 & e_2 & \dots & e_n \\ \hline \end{array}$$

Here each  $c_i$  is a Boolean expression, when  $c_i$  is true  $f$  returns  $e_i$

- Disjointness -  $i \neq j \rightarrow (c_i \wedge c_j \leftrightarrow \perp)$
- Completeness -  $(c_1 \vee c_2 \vee \dots \vee c_n) \leftrightarrow \top$



# Tabular Expression Semantics

Tabular expressions have a well defined semantics. Recently [Jin and Parnas, 2010] has defined a consistent semantics for all known tabular expression types used in practice.

A table type is defined by:

- 1 Constituents - dimensions, indexed-sets giving condition grids and results grids
- 2 Auxiliary functions - how grids are evaluated or properties constituents should satisfy, e.g., predicate to evaluate if grid is “Proper”
- 3 Restriction schema - e.g. complete and disjoint condition headers for normal function tables
- 4 Evaluation schema - formal semantics of how you evaluate a table type.



# Why Tables Work

## Example

$$f(x, y) \stackrel{\text{df}}{=} \begin{cases} x + y & \text{if } x > 1 \wedge y < 0 \\ x - y & \text{if } x \leq 1 \wedge y < 0 \\ x & \text{if } x > 1 \wedge y = 0 \\ xy & \text{if } x \leq 1 \wedge y = 0 \\ y & \text{if } x > 1 \wedge y > 0 \\ x/y & \text{if } x \leq 1 \wedge y > 0 \end{cases} \quad (1)$$

$$f(x, y) \stackrel{\text{df}}{=} \quad (2)$$

	$x > 1$	$x \leq 1$
$y < 0$	$x + y$	$x - y$
$y = 0$	$x$	$xy$
$y > 0$	$y$	$x/y$

You can actually read them.



# TTS: The Table Tool System

## [Parnas and Peters, 1999]

- Developed by Parnas et al to demonstrate possibilities of table tools
- tried to support every type of table - but did not at that time have a consistent semantics for all table types
- was an academic tool - with all that implies

# NRL's SCR\* Toolset

- Build on tabular methods used on the A-7 [Heninger, 1980] project.
- Heitmeyer *et al.* have made extensive use of the Software Cost Reduction (SCR) tabular methods supported by the “light-weight” SCR\* tool suite
- Used extensively for the creation and analysis of requirements for industrial and military software applications (e.g., [Heitmeyer et al., 1998]).
- allows users to to incorporate more heavy duty analysis tools such as the explicit state model checker SPIN [Holzmann, 1997] with SCR\*.
- Closed source, restrictive license, not commercially available toolset.

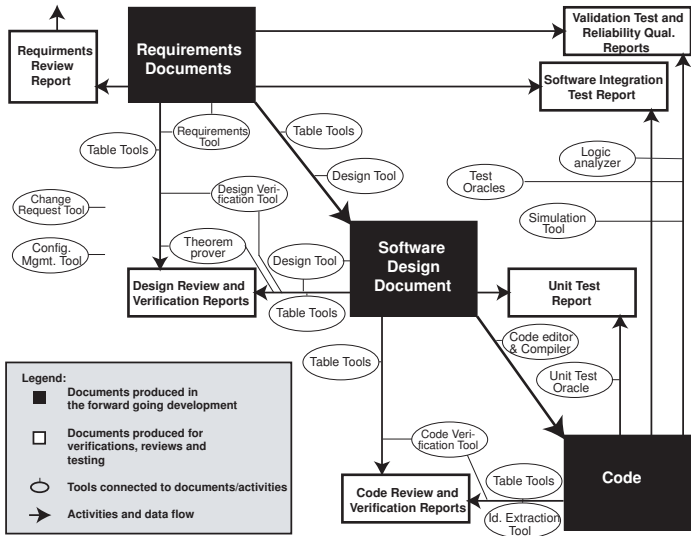
# Fillmore Tools Eclipse Plugin [Peters et al., 2007]

To Do Table Tool Right We Tried to:

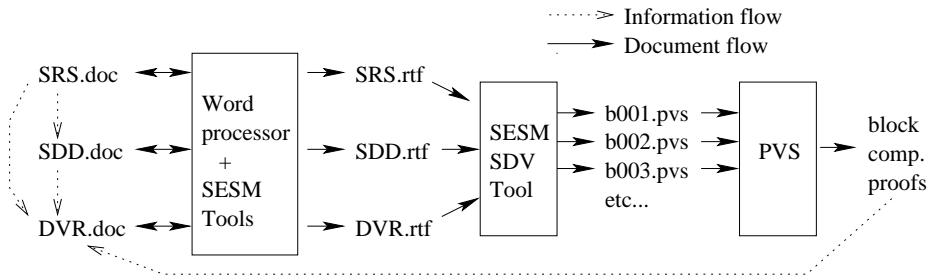
- 1 have a comprehensive table semantics **Parnas et al 2006**
- 2 use “Standard” way to encode semantics in documents **OMDoc**
- 3 have tools for verification, test case & code generation  
**PVS,FCN,...**
- 4 with a means of translating semantic content between these tools  
**XSLT**
- 5 in a tool developers actual use to tie it all together **Eclipse**

Problem: This still takes a lot of heavy lifting to get a usable tool.

# How were tables used for Darlington NGS?



# Table Tool Workflow on Darlington NGS SDS



Good:	Bad:
Tables were readable	but tedious to create
Tools found errors	but difficult to use & interpret errors
Had “formal” semantics	But AECL/OPG built EVERYTHING!

# Tabular Expression Toolbox (TET)

Decided to develop a Matlab/Simulink toolbox:

## Advantages:

- Model Based Design has been shown to reduce cost and improve quality of software development
- Focus engineer's time on early life cycle processes (modeling, simulation, analysis), and automate late life cycle activities (coding, testing)
- Matlab/Simulink industry standard.
- Advanced code generation tools for C, VHDL, Verilog.
- Existing research/tools on adding formalizations to Simulink.

## Pitfalls:

- Semantics of MBD tools are dubious and a moving target
- It's an *academic tool* (but it's open source)

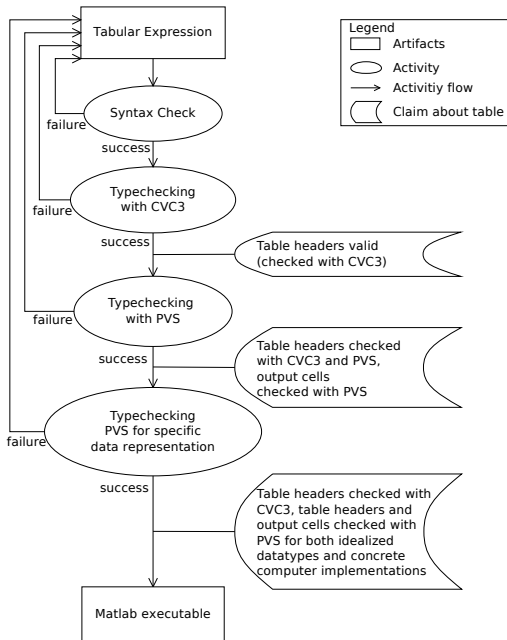


# Tabular Expressions Toolbox (TET)

- Provides Simulink block for creating tabular expressions.
- GUI for creating 1/2D tables with nested headers, single/multiple outputs.
- Supports code generation through embedded Matlab language.
- Integrates with CVC3 SMT solver and PVS theorem prover for checking disjointness and completeness conditions.
- For improper tables, tool attempts to generate counter example and clearly show user why table is improper.
- Available Now!

<http://www.mathworks.com/matlabcentral/fileexchange/28812-tabular-expression-toolbox>





## TET Workflow

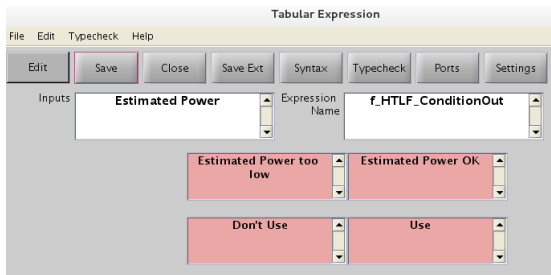
- 1 Perform quick Matlab syntax check
- 2 Checking with CVC3 on  $\mathbb{R}$  is fast, often detects logic errors in headers
- 3 Checking with PVS on  $\mathbb{R}$  provides additional assurance, type checks results grid
- 4 Checking with PVS on IEEE floats shows absence of under/overflow, etc.

# Tabular Expression Formats & Features

- The tool supports one dimensional and two dimensional normal function tables
- tool supports multiple output for single dimensional tables, and single output for 2 dimensional tables,
- supports nested headers (Parnas' "circular" tables) along one dimension.
- has limited "undo" feature for both expression edits and graphical (i.e. delete a row) edits.

# Matlab syntax checking and Visual Highlighting

Clicking on the “Check” button uses Matlab’s Syntax checking highlights in **red** any cells with syntax errors or that are empty.



## Requirement

*Heat Transport Low Flow Trip shall be Conditioned Out at low power*



# Save to M-file

From the table edit window, selecting `File -> Save to M-file`

- Immediately lets you execute your specification
- You can generate C code or HDL code
- You can apply other formal tools - e.g. Polyspace, etc.

# Completeness and Consistency Checking

When checks fail, getting useful information about why is important.

- Counter examples currently generated by
  - CVC3 SMT solver, and/or
  - PVS' "random-test" feature.
- Gives input values for counter example, and
- graphical feedback highlighting the error.

**Red** is used to show conditions in headers that the counter example makes **FALSE**.

**Green** is used to show conditions in headers that the counter example makes **TRUE**.

# Counter Example Generation: Completeness

Tabular Expression

File Edit Typecheck Help

Edit Save Close Save Ext Syntax Typecheck Ports Settings

Inputs **f\_Estimated\_Power, k\_HTLF\_PowerSP** Expression Name **f\_HTLF\_ConditionOut**

**f\_Estimated\_Power < k\_HTLF\_PowerSP** **f\_Estimated\_Power > k\_HTLF\_PowerSP**

false true

CVC3 Report

**Typecheck Summary** 1 of 1

Prev Next

TCC Name	Sequent	Counter Example
f_HTLF_ConditionOut.cv c_2_COM	QUERY ((f_Estimated_Power < k_HTLF_PowerSP) OR (f_Estimated_Power > k_HTLF_PowerSP));	f_Estimated_Power = 0; k_HTLF_PowerSP = 0;

# Counter Example Generation: Disjointness

The screenshot shows the Tabular Expression Toolbox interface. The main window displays a table with three columns: Inputs, Expression Name, and Expression. The first row shows inputs `f_Estimated_Power, k_HTLF_PowerSP, k_Hys, Previous` and the expression name `f_HTLF_ConditionOut`. The second row shows the expression `f_Estimated_Power < k_HTLF_PowerSP || f_Estimated_Power >= k_HTLF_PowerSP + k_Hys` with a green background. The third row shows the expression `f_Estimated_Power >= k_HTLF_PowerSP` with a red background. Below the table, the values `false`, `Previous`, and `true` are displayed.

A CVC3 Report window is overlaid on top, showing a Typecheck Summary. The summary includes the following information:

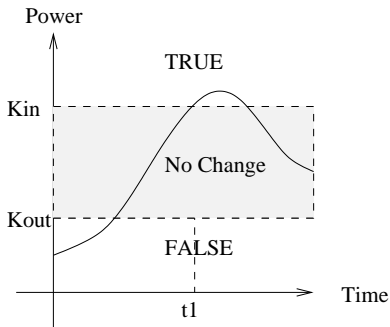
TCC Name	Sequent	Counter Example
f_HTLF_ConditionOut.cv c_1.DIS	<pre> QUERY (NOT ((f_Estimated_Power &lt; k_HTLF_PowerSP) AND (f_Estimated_Power &gt;= k_HTLF_PowerSP OR f_Estimated_Power &lt; k_HTLF_PowerSP+k_Hys) AND NOT ((f_Estimated_Power &lt; k_HTLF_PowerSP) AND </pre>	<pre> f_Estimated_Power = 0; k_HTLF_PowerSP = 1; k_Hys = 0; Previous = 0; </pre>

## Requirement

*Use hysteresis on setpoints to eliminate sensor chatter.*



# PVS (sub)Typing



$\text{PwrCond}(\text{Prev}:\text{bool}, \text{Power}, K_{in}, K_{out}:\text{posreal}):\text{bool} =$

$\text{Power} \leq K_{out}$	$K_{out} < \text{Power} < K_{in}$	$\text{Power} \geq K_{in}$
<i>FALSE</i>	<i>Prev</i>	<i>TRUE</i>

# PVS (sub)Typing

The screenshot shows the PVS software interface with a window titled "f\_PowerCond". The interface includes a menu bar (File, Edit, PVS, Help) and a toolbar with buttons for Edit, Save, Close, Save Ext, Check, PVS, Ports, and Settings. The main workspace contains an "Inputs" field with the text "Power,Kin:real,Kout:real,Prev:bool" and an "Expression Name" field with "f\_PowerCond". Below these are three colored boxes: a green box containing "Power <Kout", a red box containing "Kout <= Power && Power < Kin", and another green box containing "Kin <= Power". Underneath are three white boxes labeled "false", "Prev", and "true".

An overlaid window titled "PVS Report" displays a "Typecheck Summary" for "1 of 1" items. It includes buttons for "Open P...", "Prev", and "Next". The report is structured as follows:

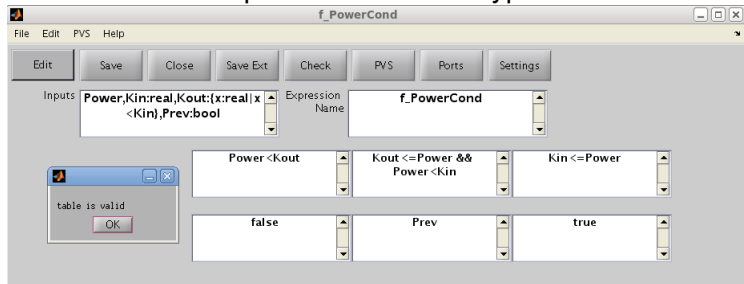
TCC Name	Sequent	Counter Example
f_PowerCond_TCC1	<pre>f_PowerCond_TCC1 :  ----- {1} FORALL (Power, Kin, Kout: real):   NOT (Power &lt; Kout AND Kout     &lt;= Power AND Power &lt; Kin) AND   NOT (Power &lt; Kout AND Kin     &lt;= Power) AND   NOT ((Kout &lt;= Power AND     Power &lt; Kin) AND Kin &lt;= Power)</pre>	<pre>Power = 1.0513; Kin = 0; Kout = 1.1829;</pre>

At the bottom left of the PVS Report window, the status is indicated as "Status: Not Typechecked".

# PVS (sub)Typing

Problem occurred because developer implicitly assumed that  $Kout < Kin$ .

We can make it explicit with PVS subtypes.



Note: We still need to develop tool to check input typing is satisfied when table with PVS subtyping is used!

# Counter Example Generation: Results Defined

Tabular Expression

File Edit Typecheck Help

Edit Save Close Save Ext Syntax Typecheck Ports Settings

Inputs  $x, y$  Expression Name  $f\_resultCheck$

$x > 1$   $1/(x+y+1)$

$x \leq 1$   $1/x$

PVS Report 2 of 2

**Typecheck Summary** Open P... Prev Next

TCC Name	Sequent	Counter Example
$f\_resultCheck\_TCC2$	$f\_resultCheck\_TCC2 :$ $ -----$ $(1) \text{ FORALL } (x: \text{real}): x \leq 1 \text{ IMPLIES } x \neq 0$	$x = 0;$

Status: No

Proof of well definedness of divide operation fails for  $x = 0$

# Problems with Floating Point values

- floating point numbers have maximums and minimums
- floating point arithmetic does not have the same axioms as under reals, ie. associativity does not hold.
- Operations on floats will, under certain scenarios, produce a reserved value NaN which cannot be compared to other values.

# Reals vs Floating Points

- Both PVS and CVC3 have support for the theoretical reals.
- In the case of PVS almost all the existing theories and strategies involve the real type.
- Using the reals is much faster and more intuitive than attempting a floating point representation for proving tables.
- Our approach involves using step-wise refinement:
  - 1 Initial type checking shall be performed using the real type, this will uncover many of the logical errors.
  - 2 Further refinement involves using lower level representations (floats, integers, etc.) This step will discover possible overflow, rounding, and other errors common with numerical representations.

## An Example: Numerically improved quadratic solver

## Example

Conditions		Results			
		<b>solution caption</b>	$x_1$	$x_2$	
$a = 0$	$b = 0$	Ill defined	$NaN$	$NaN$	
	$b \neq 0$	One Root	$-\frac{c}{b}$	$NaN$	
$a \neq 0$	$b^2 - 4ac < 0$		No Real Roots	$NaN$	
	$b^2 - 4ac = 0$		One Root	$-\frac{b}{2a}$	
	$b^2 - 4ac > 0$	$b \geq 0$	Two Roots	$\frac{-b - \sqrt{b^2 - 4ac}}{2a}$	$\frac{c}{ax_1}$
		$b < 0$	Two Roots	$\frac{-b + \sqrt{b^2 - 4ac}}{2a}$	$\frac{c}{ax_1}$



# Example: Quadratic Solver (continued)

- Table will typecheck using reals on PVS (using dependent subtyping)
- Using floating point numbers the completeness condition fails
  - $b^2 - 4ac$
  - for input scenario  $b^2 \rightarrow +\infty \wedge 4ac \rightarrow +\infty$  we get  $\infty - \infty$  which produces NaN.
  - When NaN compared to 0 always false
  - Can occur if

$$b > \pm \sqrt{2^{E_{max}+1} - 2^{E_{max}+1-p}} \vee ac > 2^{E_{max}+1} - 2^{E_{max}+1-p}$$

- Need to subtype the inputs to restrict their range in order to guarantee that table is complete and disjoint.
- We can test this example in Matlab and show that the wrong result will be given.



# Solution

- Using the NASA floating point libraries for PVS we've developed some lemmas to assist with proving the correctness of floating point expressions
- For purposes of checking completeness and disjointness we want to make sure that expressions evaluate to either a finite number or infinity.
- proved the above table is proper for single floating point numbers with subtype  $b^2 : \text{finite}$
- A sample lemma:





```
op_add_inf_fin_1: LEMMA FORALL (mode:rounding_mode,
  x:{fp:fp_num|finite?(fp) OR infinite?(fp)},
  y:{fp:fp_num| finite?(fp)}):
  finite?(fp_add(x,y,mode)) OR infinite?(fp_add(x,y,mode))
```

# On Fixing the Weakest Link for TET

- Currently Tabular Expression Toolbox only provides “local” block check that tabular expression is well defined.
- Really need to type check block interconnections for values, measures & units
- SRI’s SimCheck tool [Roy and Shankar, 2011] provides these features for Simulink diagrams
- Plan to work with SRI to integrate TET & SimCheck to provide graphical local and global verification capabilities

# Conclusions

- Tabular expressions toolbox makes it easier to use tabular expressions and increases confidence in models
- You can “Hide the Formal Verification” under the hood so the software developers will use them!
- Need to verify inter-block typing with something like SimCheck [Roy and Shankar, 2010].
- Use of formal verification at design time is very useful . . . what are the implications for independence of design and verification teams?
- I am sweeping a lot of the nasty Matlab semantics issues under the rug - though I think you can restrict to a safe subset of Matlab as in [Whalen et al., 2008].

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