

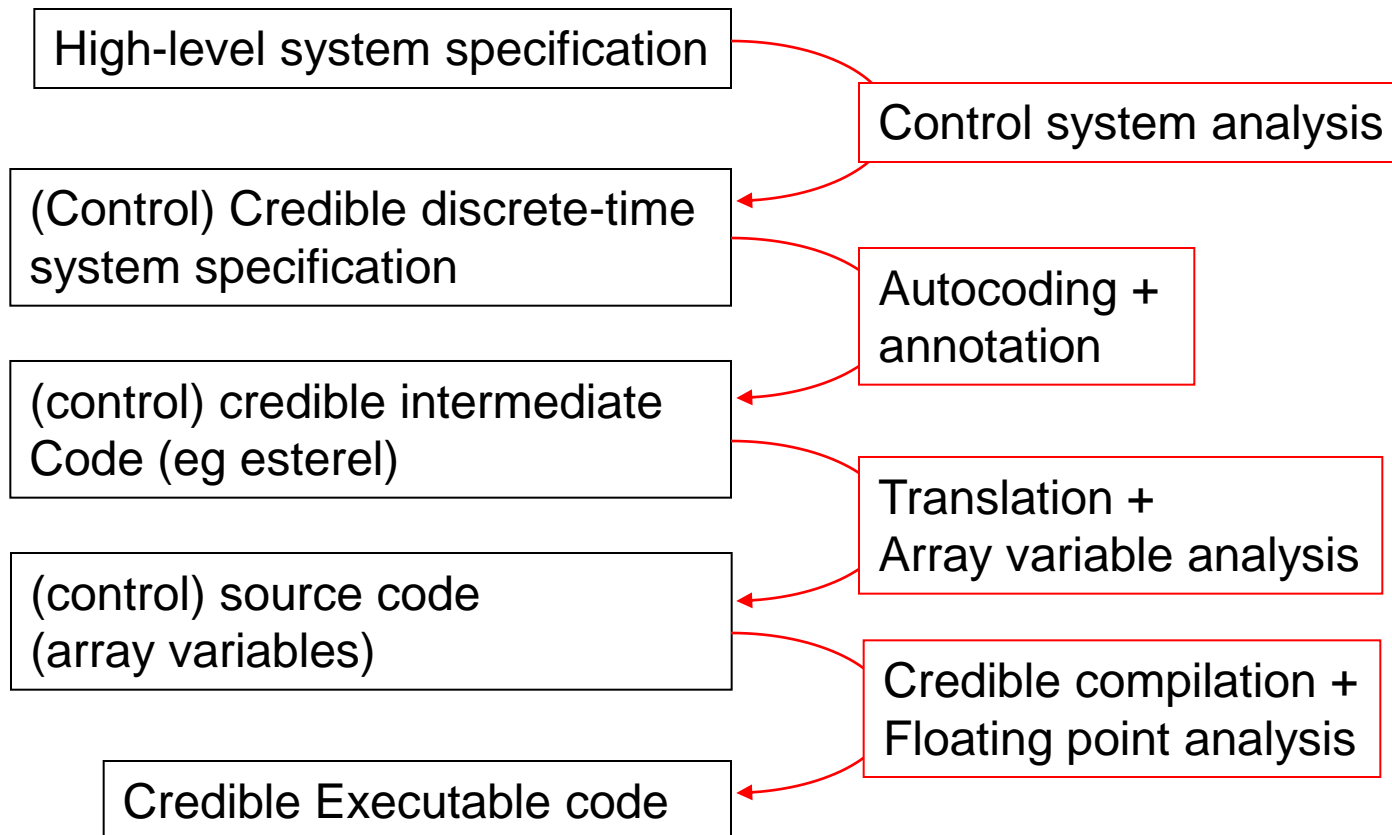
# A cascaded approach to control system software design and verification

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# Main message

- We propose an hierarchical approach to control system V&V.



# Some Background: Control systems

- Much ongoing progress in control system analysis and design:
  - Nonlinear control systems
  - Adaptive controls
  - Robustness analyses
  - Decentralized control systems
- Novel Control systems often come with rigorous proofs and spectacular application.
- Control specs flow down to software design, proofs do not and must be reconstructed from scratch (see ASTREE example next)

# Some background: Software analysis

- Many static software analyzers over past few decades.
- Industrial implementation of model checkers.
- Application of abstract interpretation techniques (ASTREE analyzer) for Airbus A380 control code.

From Feret, 2004

Static Analysis of Digital Filters

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A simplified second order filter relates an input stream  $E_n$  to an output stream defined by:

$$S_{n+2} = aS_{n+1} + bS_n + E_{n+2}.$$

Thus we experimentally observe, in Fig. 4, that starting with  $S_0 = S_1 = 0$  and provided that the input stream is bounded, the pair  $(S_{n+2}, S_{n+1})$  lies in an ellipsoid. Moreover, this ellipsoid is attractive, which means that an orbit starting out of this ellipsoid, will get closer of it. This behavior is explained by Thm. 5.

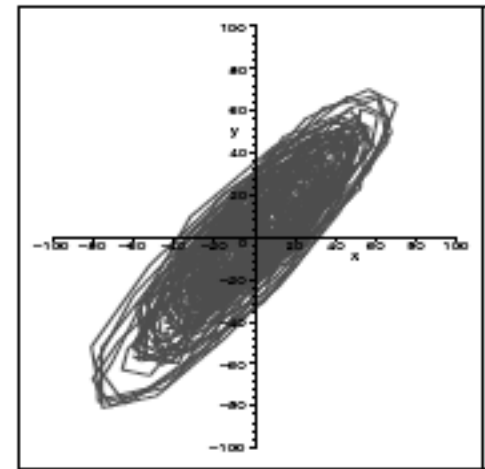


Fig. 4. Orbit.

Control specifications semantics trickle down to software implementation.

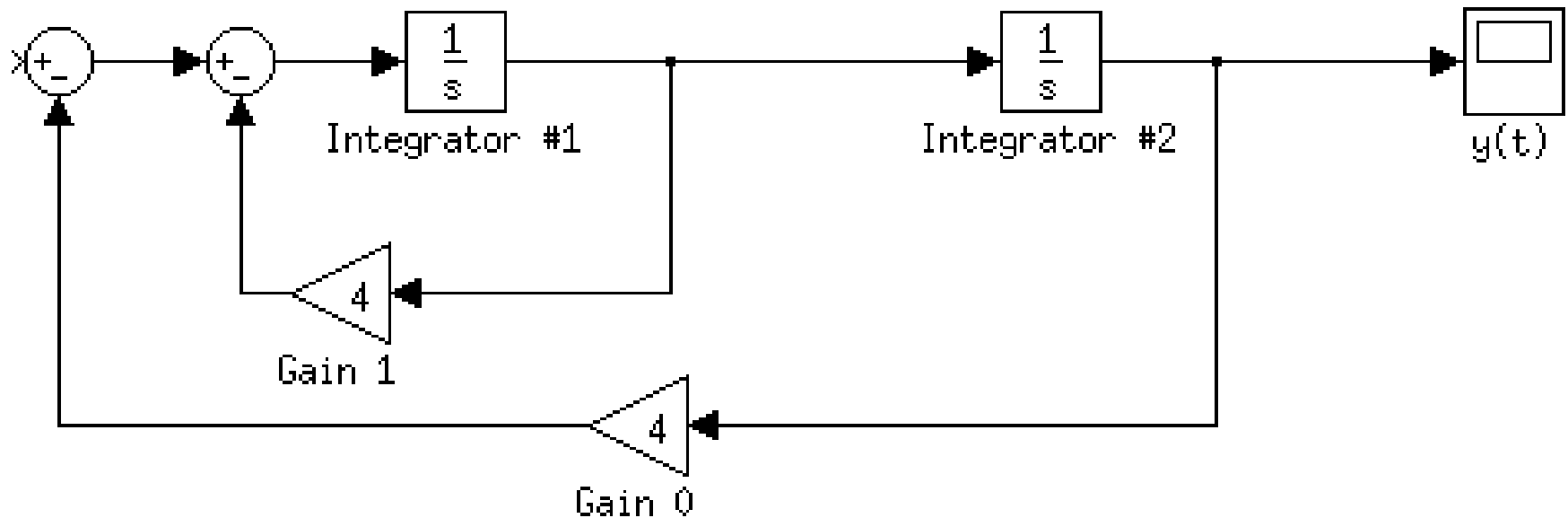
# Core idea

- Develop an environment where control/software engineers can implement control laws (and other, many other control-oriented products) and flow them down seamlessly to executable software, together with formal proofs of correctness.
- Call that “credible autocoder” to emphasize need to produce formally proven software.
- Properties of specifications/software are extracted as soon as possible and flow down with successive implementations.

# Process

- We begin with a control systems, user-friendly interface (eg simulink-like).
- We end with a commented source code.

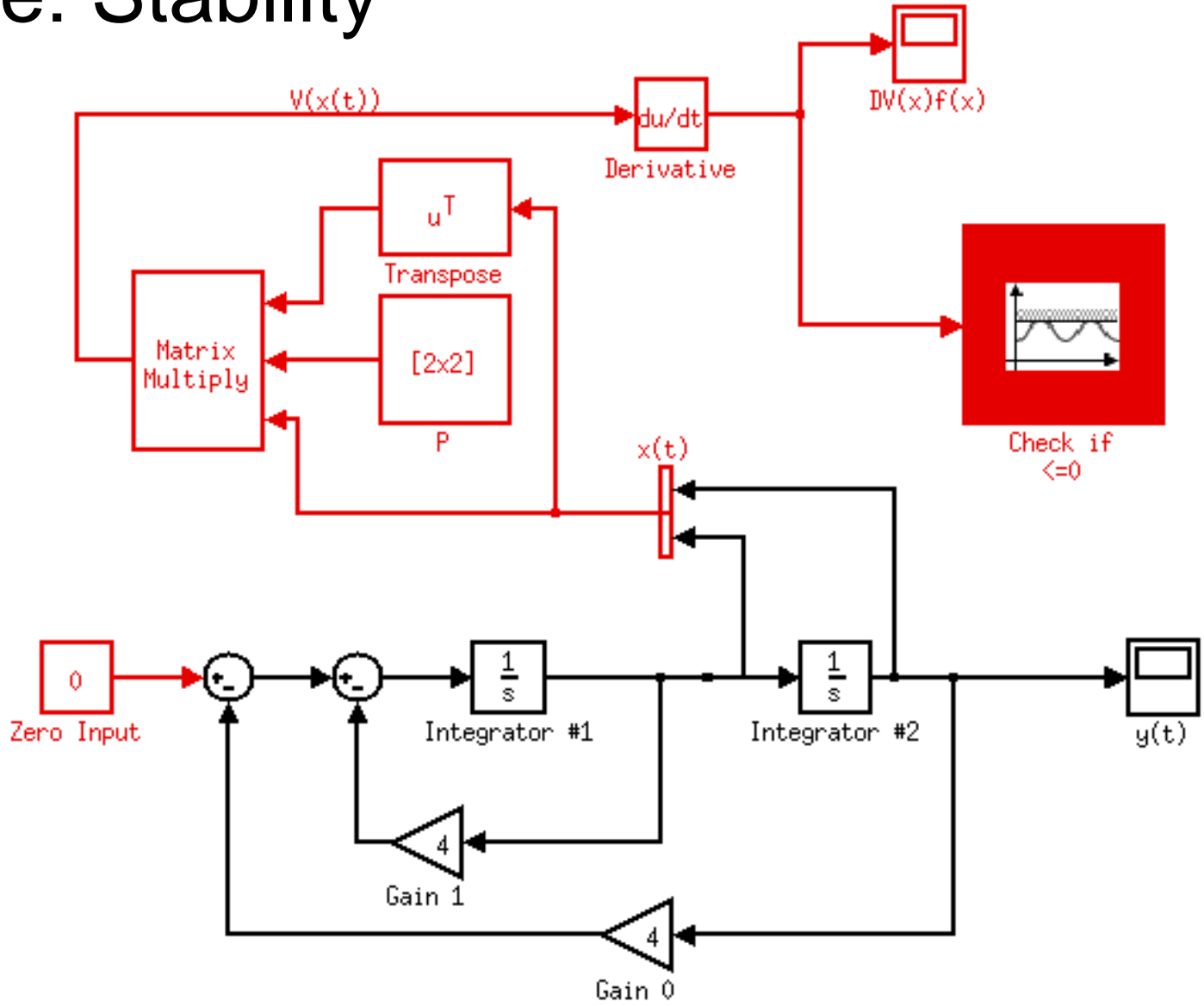
# Specification level: Graphical interfaces



Typical simulink diagram comes with no comments/semantics

Can use commenting capabilities of Simulink to illustrate design process

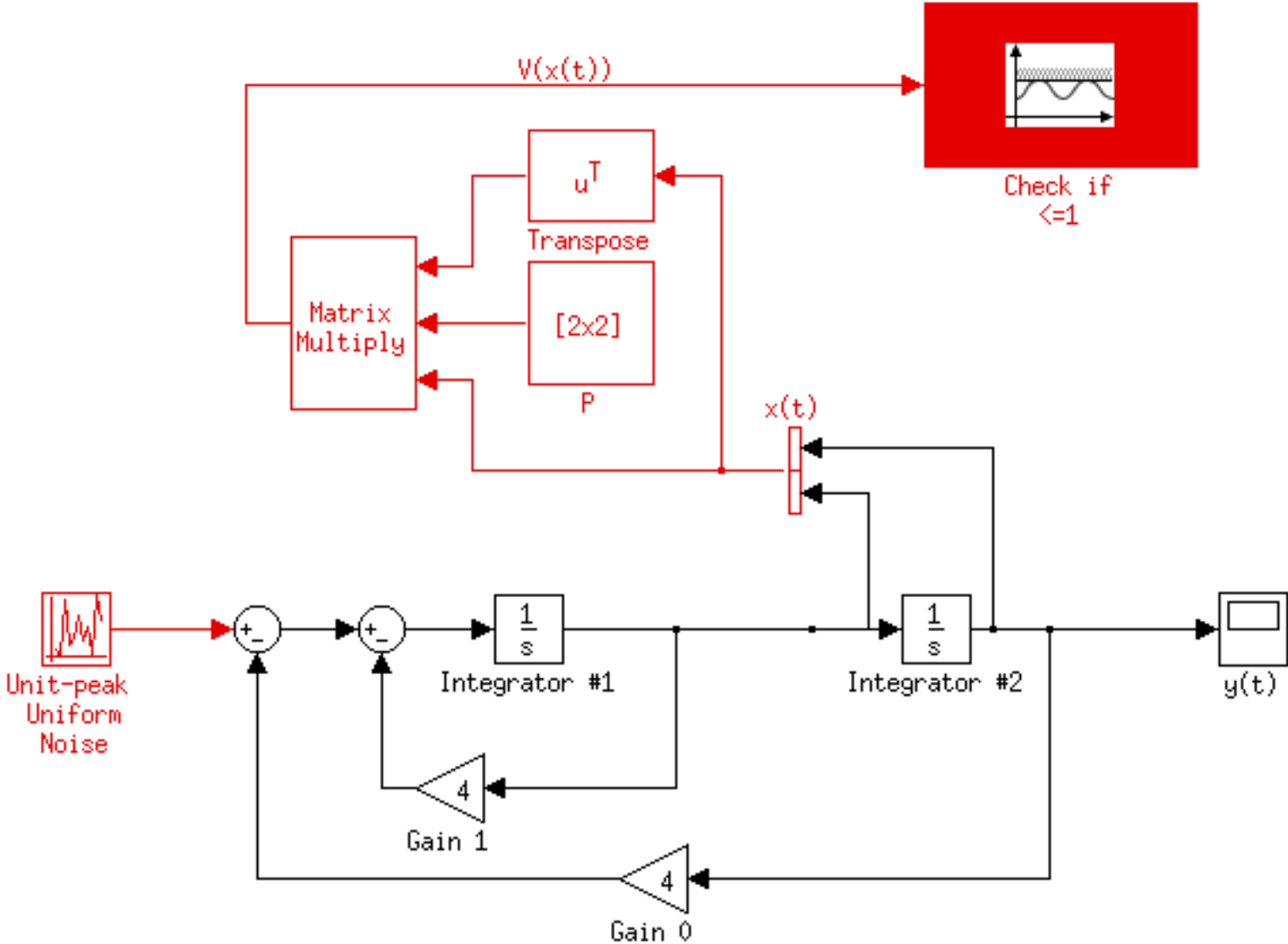
# Example: Stability



$$\frac{d}{dt} x^T P x < 0, \quad P > 0$$

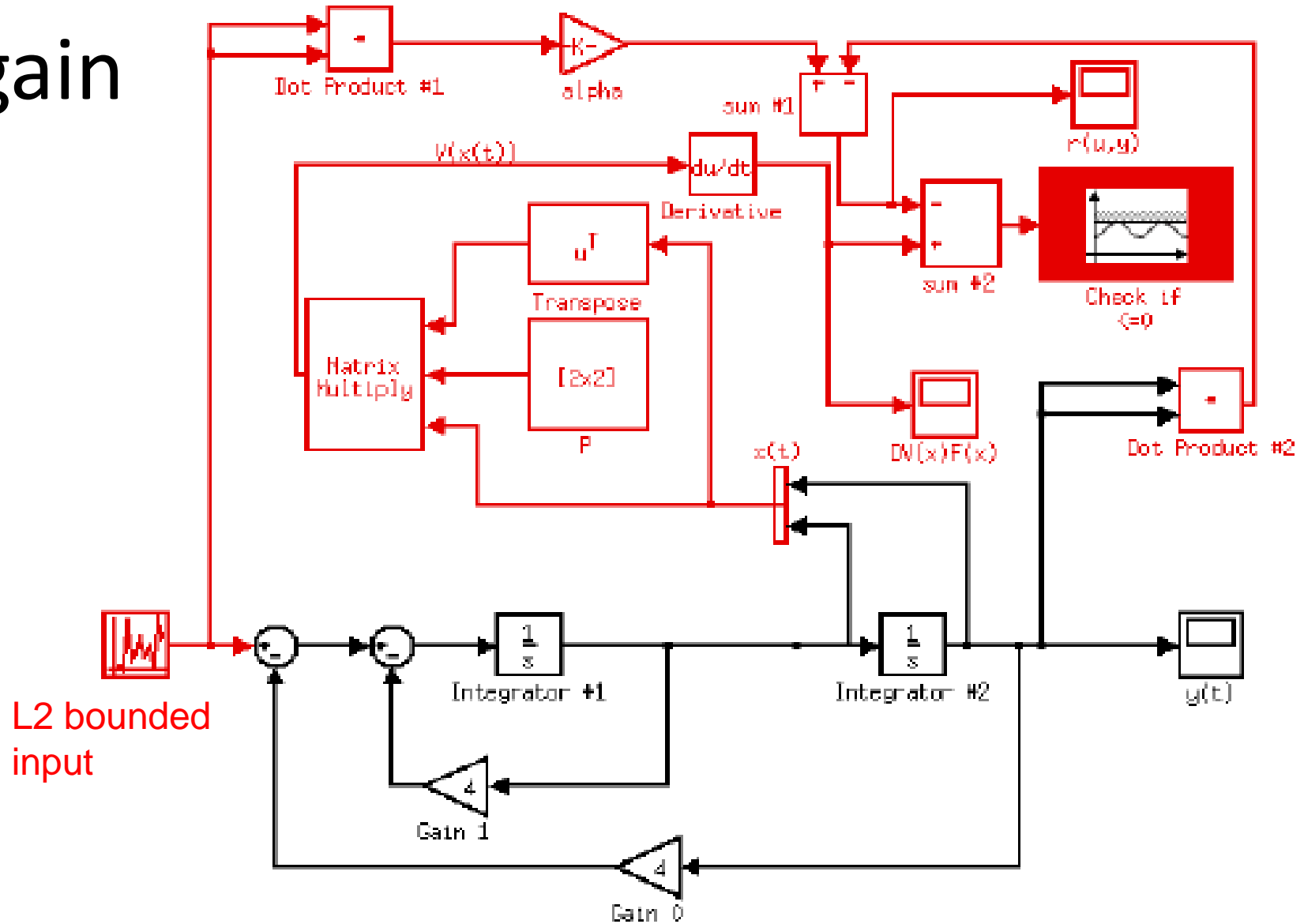


# State boundedness under bounded inputs



$$x^T P x \leq 1, \quad P > 0$$

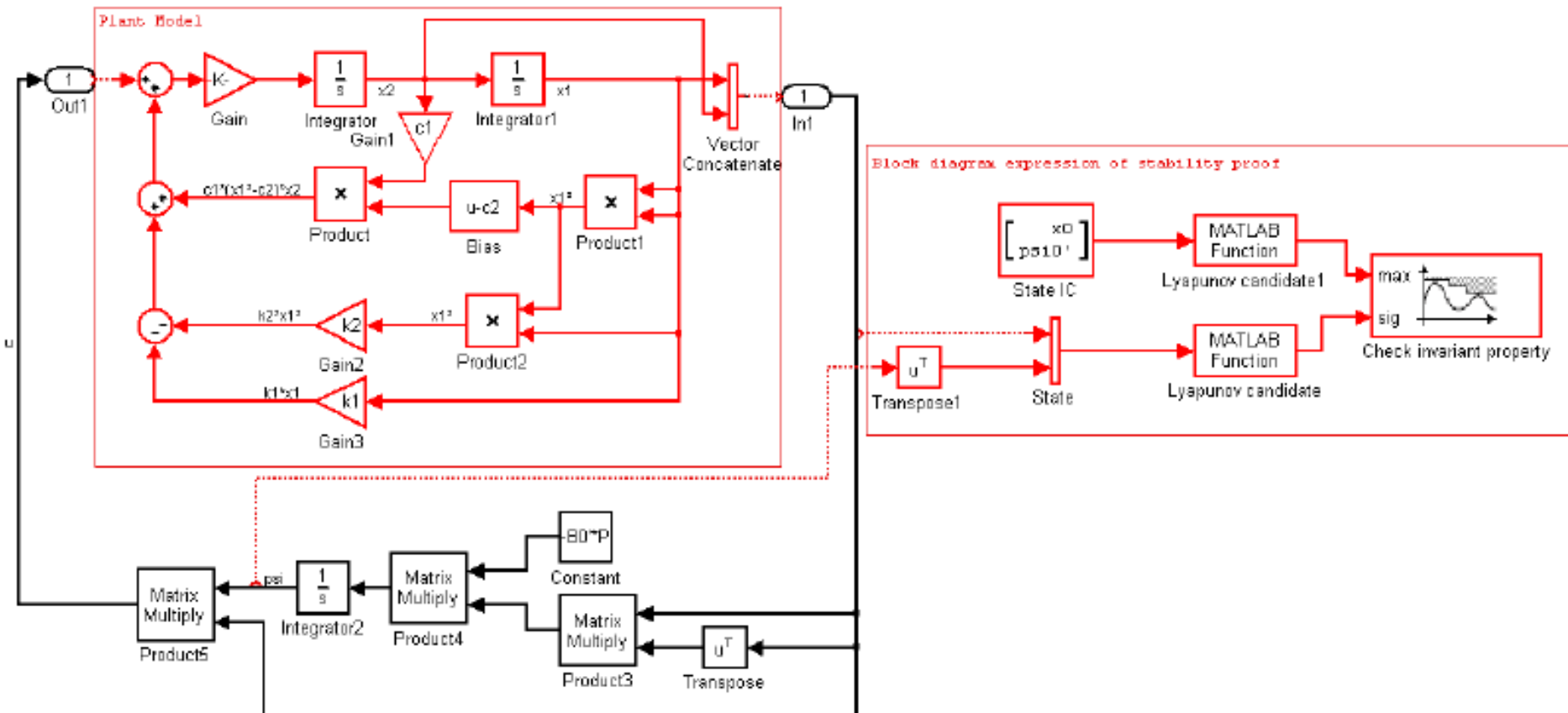
# L2 gain



$$\frac{d}{dt} x^T P x - \alpha^2 w^T w + y^T y \leq 0 \Rightarrow \int_0^\infty y^T y \leq \alpha^2 \int_0^\infty w^T w$$

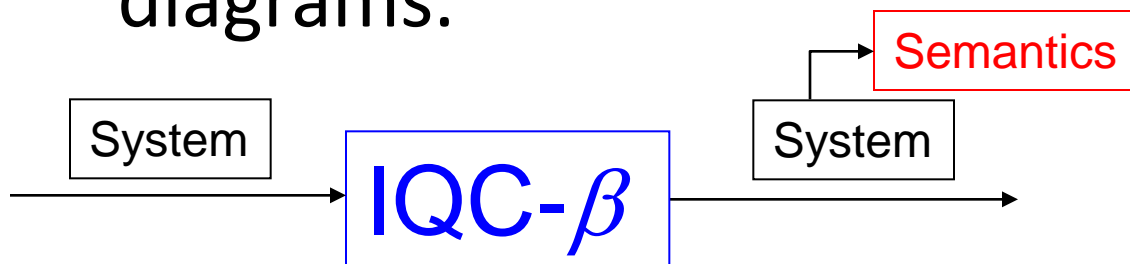
# Adding the controlled plant as part of the controller's semantics

Closed-loop adaptive control system stability



# Using third party system analysis tools

- Sometimes, control system properties can be established via automated analysis; “Specification level static analysis”
- $\mu$ -tools, IQC- $\beta$
- Automatic generation of Lyapunov functions that establish (stability, boundedness, performance) without human intervention.
- Can be used to automatically generate earlier diagrams.



# Further steps

- Discretization, expression in intermediate language, expression in source code all introduce additional analysis requirements.
- Time discretization: Lyapunov stability proofs that hold for continuous systems may hold for discretized system (most of the time). Often fails (adaptive systems, low sampling rate). Solution may require doing discrete Lyapunov-like analysis from scratch (easy for the most part).

# Further steps (ct'd)

- Expression in intermediate language: eg Matlab or SCADE.

```

{true}
1:  Ac = [0.4990, -0.0500; 0.0100, 1.0000];
{true}
2:  Cc = [564.48, 0];
{true}
3:  Bc = [1;0];D=-1280
{true}
4:  xc = zeros(2,1);
{xc ∈ EP}
5:  receive(y);
{xc ∈ EP}
6:  receive(yd);
{xc ∈ EP}
7:  yc = y-yd;
{xc ∈ EP}
8:  while 1
{xc ∈ EP}
9:  yc = max(min(y,1),-1);

```

```

{xc ∈ EP, yc2 ≤ 1}
10:  u = Cc*xc+Dc*yc;
{xc ∈ EP, yc2 ≤ 1,
(Acxc + Bcyc)T P(Acxc + Bcyc)
-0.01xcT P xc - 0.99yc2 ≤ 0,
u2 ≤ 2(CcP-1CcT + Dc2)}
skip
{xc ∈ EP,
yc2 ≤ 1}, u2 ≤ 2(CP-1CT + D2)
11:  xc = Ac*xc + Bc*yc;
{xc ∈ EP, u2 ≤ 2(CP-1CT + D2)}
12:  send(u)
{xc ∈ EP}
13:  receive(y);
{xc ∈ EP}
14:  receive(yd);
{xc ∈ EP}
15:  yc = y-yd;
{xc ∈ EP}
16:  end
{false}

```

# Translation to source code

- Must preserve semantics identified in block diagrams, carried into MATLAB: Translator currently under development at Georgia Tech.
- Translation to source code introduces many new variables. Eg Matlab has highly vectorized operations and vector variables, C uses arrays, array indices, pointers, casts etc. Coherence of these variables (range...) must be verified independently. Use static analyzers such as global C surveyor (Developed at NASA by Carnegie-Mellon West)

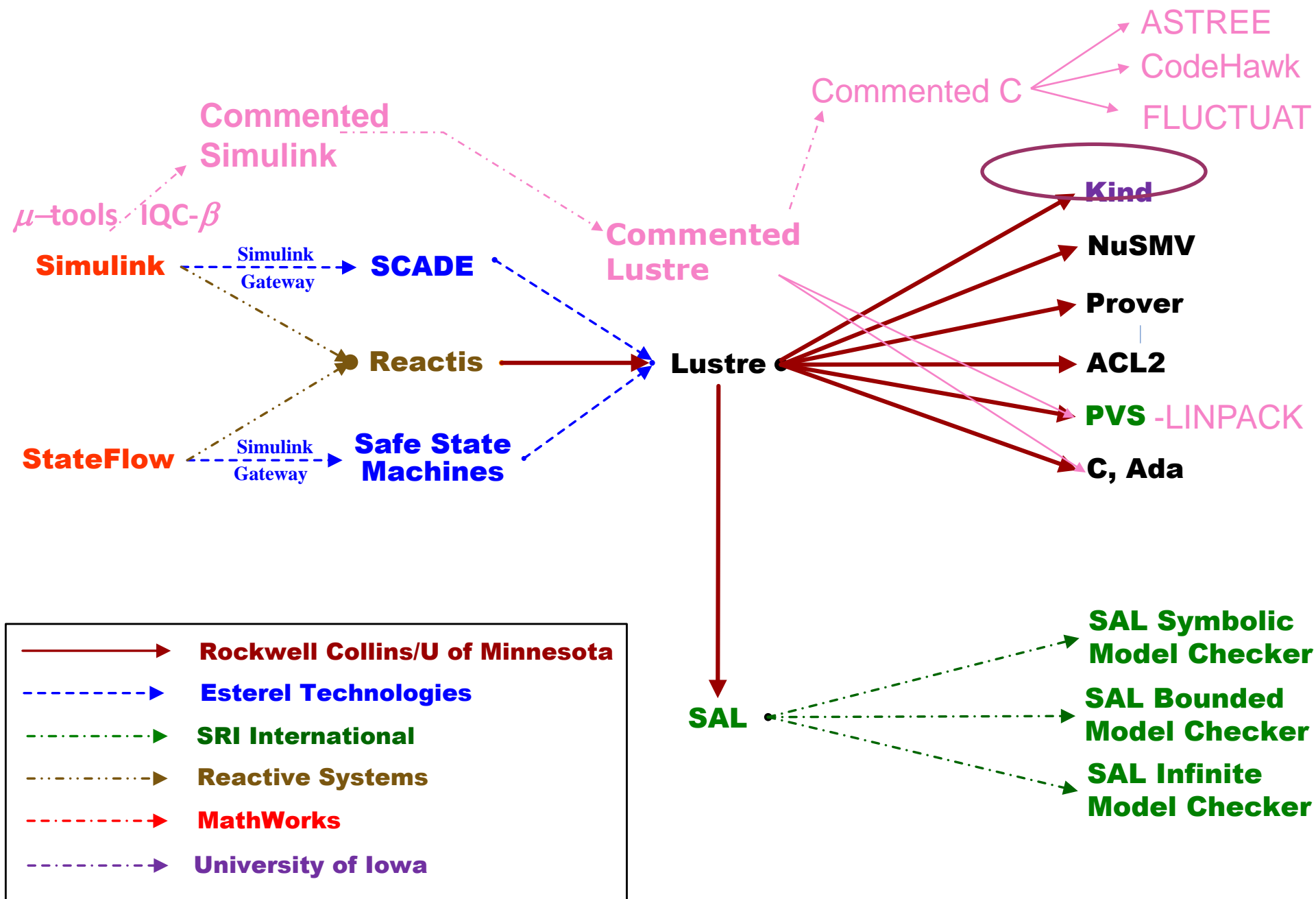
# From source code to binary

- Detailed structure of operations is known; enables detailed analysis of numerical computations with floats.
- Begin effort with executable version of FLUCTUAT (CEA/LIST --- Goubault/Putot)
- Research issue: Need to revisit high-level invariants determined earlier (quadratic inequalities), based on real computations.





# Insertion in Rockwell Collins Translation Framework



# Academic Example

- Quanser 3 DOF Helicopter



- Easy to fly and demonstrate advanced control concepts. Currently undergoing task of implementing credible controller written in C

# Industry Example

- F-18 R/C subscale model, with Rockwell Collins Damage Tolerant Flight Control Technology



- Use verifiable autocoder to generate source code for a subset of damage tolerant control laws, e.g. Lyapunov-based model reference adaptive control for lateral-directional dynamics